

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: TSUTOMU JSHIKAWA, ET AL.

SERIAL NO.: 09/552,085

FILED: February 24, 2000

FOR: SEMICONDUCTOR INTEGRATED
CIRCUIT HAVING PADS WITH LESS
INPUT SIGNAL ATTENUATION

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Group Art Unit: 2816

Examiner: Nguyen

H/15/C
11/21/02
JHM/H

AMENDMENT

The Assistant Commissioner of
Patents and Trademarks
Washington, DC 20231

Sir:

In response to the Office Action mailed May 17, 2002, Applicants request reconsideration
in the view of the following amendment and remarks for entry in the above-identified application.

FAX RECEIVED

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TECHNOLOGY CENTER 2800